

August 25, 2010

NOTICE
B Tech I YEAR TRIMESTER – I (Batch 2008-09 & 2009-10)
RE-EXAMINATION TIME-TABLE - 2010– 2011 (MUMBAI & SHIRPUR)

Timings: 2:00 pm – 5:00 pm		
Day	Date	For All Streams
Monday	Oct 25, 2010	Applied Mathematics –I (2008-09 & 2009-10)
Wednesday	Oct 27, 2010	Computer Programming – I (2008-09 & 2009-10)
Friday	Oct 29, 2010	Basic Electrical Engineering (2008-09 & 2009-10)
Saturday	Oct 30, 2010	Applied Science – I (2008-09 & 2009-10)

INSTRUCTIONS TO CANDIDATES:

1. Candidates must produce their Identity Card at the time of the examination.
2. **Candidates should know their G.R. Number which is written behind the Identity Card for appearing in the examination.**
3. Candidates will not be permitted to leave the examination hall during the first 2 hrs of the examination.
4. A Candidate will be dealt strictly if he/she is found resorting to any sort of unfair means.
5. No cell phones or any other electronic gadgets will be allowed in the examination hall that helps the student amount to unfair means.

ASHISH APTE
(CONTROLLER OF EXAMINATIONS)

August 25, 2010

NOTICE

**MBA (Tech) I YEAR TRIMESTER – I (Batch 2008-09 & 2009-2010)
RE-EXAMINATION TIME-TABLE - 2010– 2011 (MUMBAI & SHIRPUR)**

Timings :- 10.00 a.m. – 1:00 p.m.		
Day	Date	For All Streams
Monday	Oct 25, 2010	Applied Mathematics – I
Wednesday	Oct 27, 2010	Computer Fundamentals (2008-09 & 2009-2010)
Friday	Oct 29, 2010	Basic Electrical Engineering (2008-09 & 2009-2010)
Saturday	Oct 30, 2010	Applied Science - I

*** Consists of Applied Physics – I and Applied Chemistry – I**

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**B Tech II YEAR TRIMESTER – IV (Batch 2007-08 / 2008-09 / 2009-10)
RE-EXAMINATION TIME-TABLE - 2010– 2011 (MUMBAI & SHIRPUR)**

Timings: 10:00 am – 1:00 pm					
Day	Date	I.T.	Computer	Electronics	Telecom
Monday	Oct 25, 2010	Applied Mathematics – IV (2007-08 , 2008-09 & 2009-10)			
Wednesday	Oct 27, 2010	Digital Logic Design & Application (2008-09) / Digital logic Design	Digital logic Design	Advanced Electronics	
Friday	Oct 29, 2010	Data Structure & Algorithm	Data Structure & Numerical Techniques (2008-09 & 2009-10)	Network Analysis & Synthesis (2008-09) / Electrical Network Analysis & Synthesis	Electrical Network Analysis & Synthesis
Saturday	Oct 30, 2010	Principle of Communication Engineering	Microprocessor & Microcontroller	Signals & Systems	

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August 25, 2010

NOTICE

MBA Tech II YEAR TRIMESTER – IV (Batch 2008 - 09 & 2009 - 2010)
RE-EXAMINATION TIME-TABLE - 2010– 2011 (MUMBAI & SHIRPUR)

Timings :- 2:00 pm – 5:00 pm

Day	Date	IT	Chemical	Computer	Manufacturing	Telecom
Monday	Oct 25, 2010	Digital Design & Microprocessor	--	Digital Design & Computer Organization	---	----
Tuesday	Oct 26, 2010	Database Management System (2008-09 & 2009-10)	Fluid Mechanics	----	----	Database Management System (2008-09 & 2009-10)
Wednesday	Oct 27, 2010		----	-----	----	Digital Design (2008-09 & 2009-10)
Thursday	Oct 28, 2010	Data Structure & Algorithms	----	----	Strength of Material	----
Friday	Oct 29, 2010	-----	----	-----	----	Analog Integrated Circuits (2008-09 & 2009-10)

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Timings:- 2.00 p.m.. – 5:00 p.m.		
Day	Date	IT
Tuesday	Oct 26, 2010	Networking, Architecture and Communication
Thursday	Oct 28, 2010	Data Warehousing & Mining
Saturday	Oct 30, 2010	Software Engineering Methodology

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